

M.E. (Electronics)
Course Structure
Semester-I

Sr. No.	SUBJECT	TEACHING SCHEME			EXAMINATION SCHEME		
		L	T	P	PT	TW	OE
01.	VLSI Design	3	-	2	100	25	-
02.	Advanced Digital Signal Processing	3	-	2	100	25	-
03.	Advanced Network Engineering	3	1	-	100	25	-
04.	Random Signals & Processes	3	-	2	100	25	-
05.	Elective-I	3	1	-	100	25	-
06.	Seminar-I	-	-	2	-	50	-
		15	2	8	500	175	-

Semester-II

Sr. No.	SUBJECT	TEACHING SCHEME			EXAMINATION SCHEME		
		L	T	P	PT	TW	OE
01.	Advanced Computer Architecture	3	1	-	100	25	-
02.	Embedded System Design	3	-	2	100	25	-
03.	Advanced Process Control	3	-	2	100	25	-
04.	Elective-II	3	1	-	100	25	-
05.	Elective-III	3	-	2	100	25	-
06.	Seminar-II	-	-	2	-	50	-
		15	2	8	500	175	-

Semester-III

Sr. No.	SUBJECT	TEACHING SCHEME					EXAMINATION SCHEME			
		L	PR	TU	DR	Total	Theory	TW	POE	OE
1.	Seminar-III	-	1	-	-	1	-	50	-	-
2.	Dissertation Phase-I	-	4	-	-	4	-	100	-	-
		-	5	-	-	5	-	150	-	-

Semester-IV

Sr. No.	SUBJECT	TEACHING SCHEME					EXAMINATION SCHEME			
		L	PR	TU	DR	Total	Theory	TW	POE	OE
1.	Dissertation Phase-I	-	5	-	-	5	-	100	-	200*
		-	5	-	-	5	-	100	-	200

**List of Elective Subjects for semester I and II
M.E. (Electronics)**

Sr. No.	Specialization	Elective-I	Elective-II	Elective-III
1	Telecommunication engineering	Statistical Communication Theory	1) Communication Switching System. 2) Mobile Communication Engineering	Broadband Communication
2.	Computer Engineering	Design and Analysis of Algorithms.	1) Neural Networks 2) Distributed Database System	1) Web Technology 2) Software Engineering